3AMSD133-512M-R
168-Pin PC-133 Unbuffered Non-ECC DIMM
512MB 64M x 64-bit

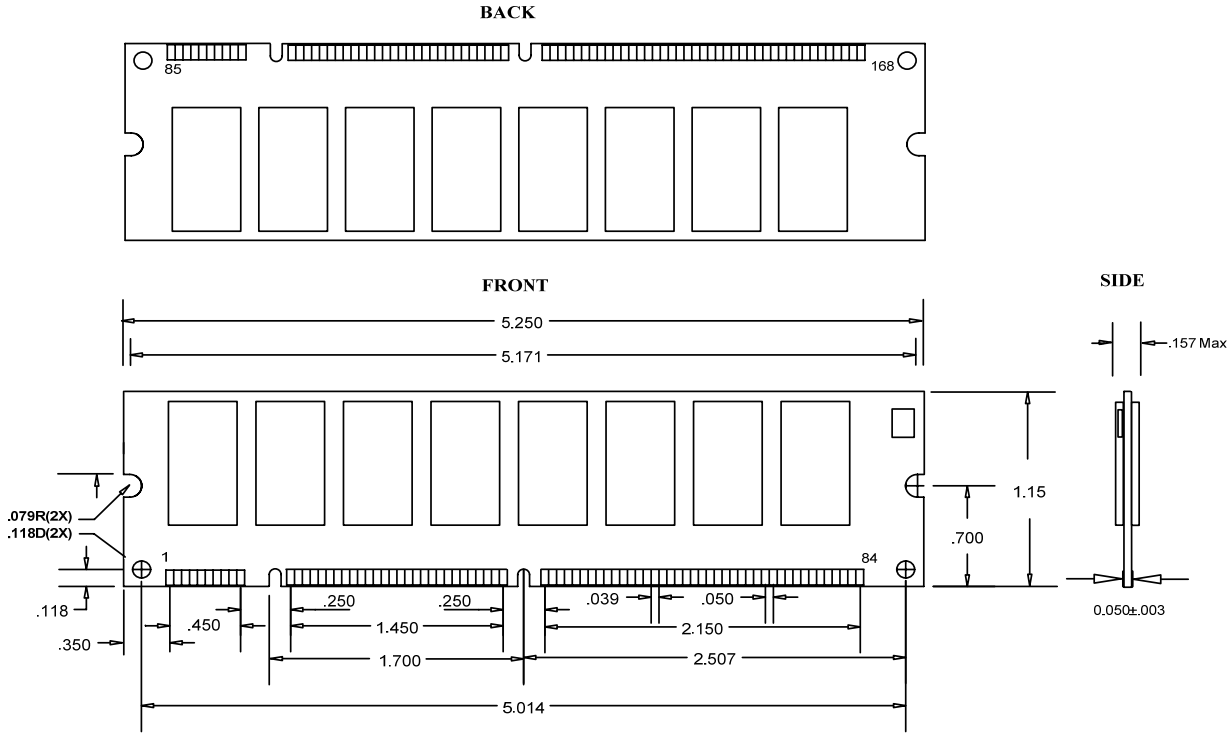
DESCRIPTION:

This data sheet describes 64Mx64-bit (512MB) PC133 SDRAM (Synchronous DRAM) memory module. It contains 16 32Mx8-bit SDRAM devices in TSOP packages. This module uses gold plated contact fingers and requires power supply of 3.3V. The electrical and mechanical specifications are shown below:

PERFORMANCE:

- Clock Cycle Time (tCK) 7.5ns
- Row Cycle Time (tRC) 65ns (min.)
- Row Active Time (tRAS) 45ns (min.) / 100,000ns (max.)
- Active to Read/Write delay (tRCD) 20ns
- Precharge command period (tRP) 20ns
- Operating Temperature 0°C to +70°C

LAYOUT:



Tolerances:+/- 0.005" unless otherwise specified

Specifications are for reference purposes only and are subject to change by Wintec Industries without notice.